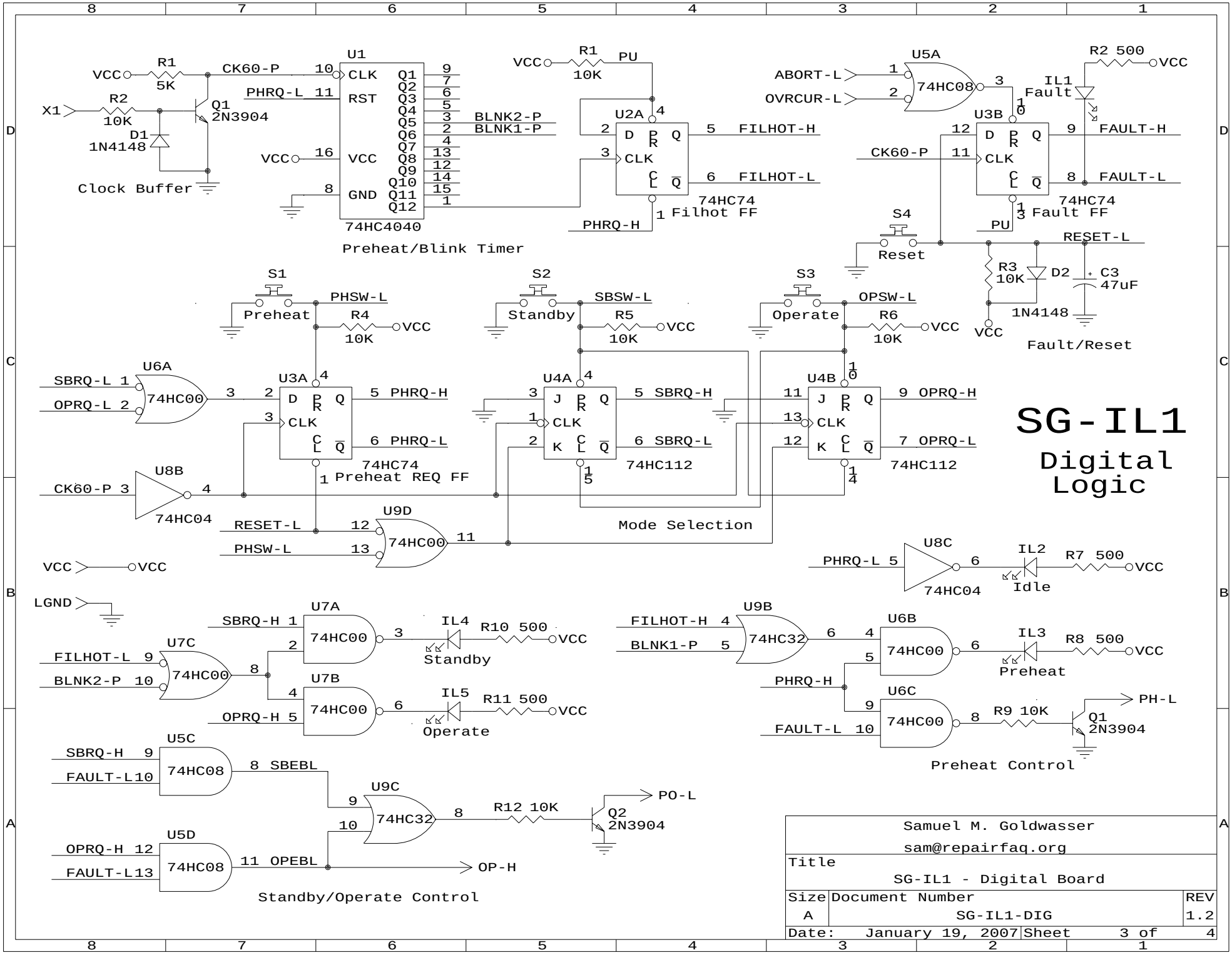
SG-IL1
Digital Logic
Clock Buffer
Preheat/Blink Timer
Filhot FF
Fault Fault FF
Fault/Reset
Preheat REQ FF
Mode Selection
Standby/Operate Control
Preheat Control
Samuel M. Goldwasser
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Title
SG-IL1 - Digital Board
Size A
Document Number SG-IL1-DIG
REV 1.2
Date: January 19, 2007
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